

L Number	Hits	Search Text	DB	Time stamp
-	48	370/382.ccls.	USPAT; US-PGPUB	2003/10/23 08:56
-	0	(stripe\$1 and aggregator and packet and (memory adj1 control\$3)) and (MCP or "master control program")	USPAT; US-PGPUB	2003/10/20 10:06
-	5	stripe\$1 and aggregator and packet and (memory adj1 control\$3)	USPAT; US-PGPUB	2003/10/20 10:06
-	6	("5774665" "5809024" "5818842" "5946313" "6028860" "6044081").PN.	USPAT	2003/10/20 10:09
-	6	("5048081" "5461614" "5587997" "5600629" "5640389" "5940596").PN.	USPAT	2003/10/20 10:11
-	0	6473433.URPN.	USPAT	2003/10/20 10:13
-	0	6473435.URPN.	USPAT	2003/10/20 10:13
-	0	6463063.URPN.	USPAT	2003/10/20 10:13
-	0	6526024.URPN.	USPAT	2003/10/20 10:14
-	3	("5987008" "6067298" "6201789").PN.	USPAT	2003/10/20 10:14
-	0	((memory adj control\$) and packet and ((command or instruction) near buffer) and switch) and (MCP or "master control program")	USPAT	2003/10/21 14:21
-	0	((memory adj control\$) and packet and ((command or instruction) near buffer) and switch) and clock near cycle) and aggregator	USPAT	2003/10/21 14:22
-	4	((memory adj control\$) and packet and ((command or instruction) near buffer) and switch) and clock near cycle) and striper or unstriper	USPAT	2003/10/21 14:23
-	74	((memory adj control\$) and packet and ((command or instruction) near buffer) and switch) and clock near cycle	USPAT	2003/10/21 14:24
-	1288	"memory controller" and packet\$1 and switch	USPAT	2003/10/21 15:11
-	259	("memory controller" and packet\$1 and switch) and ("memory controller" with buffer)	USPAT	2003/10/21 15:15
-	1	("memory controller" and packet\$1 and switch) and "memory controller" near "command buffer"	USPAT	2003/10/21 15:16
-	16	5870625.URPN.	USPAT	2003/10/21 15:19
-	11	("3771137" "5008808" "5224214" "5333276" "5379379" "5404480" "5517660" "5524220" "5524235" "5553265" "5627993").PN.	USPAT	2003/10/21 15:22
-	2823	MCP	USPAT	2003/10/22 08:28
-	550	MCP and switch	USPAT	2003/10/22 08:29
-	72	(MCP and switch) and packet	USPAT	2003/10/22 10:47
-	170	shared adj1 buffer adj1 memory	USPAT	2003/10/23 10:21
-	132	(shared adj1 buffer adj1 memory) and 370/\$.ccls.	USPAT	2003/10/23 09:39
-	12	(shared adj1 buffer adj1 memory) and 370/379,374,395.7.ccls.	USPAT	2003/10/22 11:12
-	137	(memory adj control\$) and packet and ((command or instruction) near buffer) and switch	USPAT	2003/10/22 13:02
-	4	("4847612" "5140582" "5166926" "5189666").PN.	USPAT	2003/10/22 13:08
-	2056	ASIC and memory adj1 controller	USPAT; US-PGPUB	2003/10/22 13:16

-	5	ASIC adjl architecture and memory adjl controller	USPAT; US-PGPUB	2003/10/22 13:15
-	260	striper	USPAT	2003/10/23 08:57
-	4	striper and unstriper	USPAT	2003/10/23 08:57
-	0	striper and destriper	USPAT	2003/10/23 08:57
-	4	striper and aggregator	USPAT	2003/10/23 08:57
-	60	striper and switch	USPAT	2003/10/23 08:57
-	3	(shared adjl buffer adjl memory) and 370/\$.ccls. and (memory adjl controllers)	USPAT	2003/10/23 09:39
-	3	shared adjl buffer adjl memory and (memory adj controllers)	USPAT	2003/10/23 10:21